

ABSTRACT OF THE DISCLOSURE

A DMAC is provided with a plurality of DMA transfer portions controlling DMA transfer in accordance with values set in a group of registers for current transfer. When the DMAC acquires bus ownership from a bus master, use of the bus is permitted
5 among the plurality of DMA transfer portions in a prescribed order in response to transfer requests from the plurality of DMA transfer portions. Therefore, occupation of the bus ownership by a specific DMA channel can be prevented.